

FIG. 1 (Prior Art)

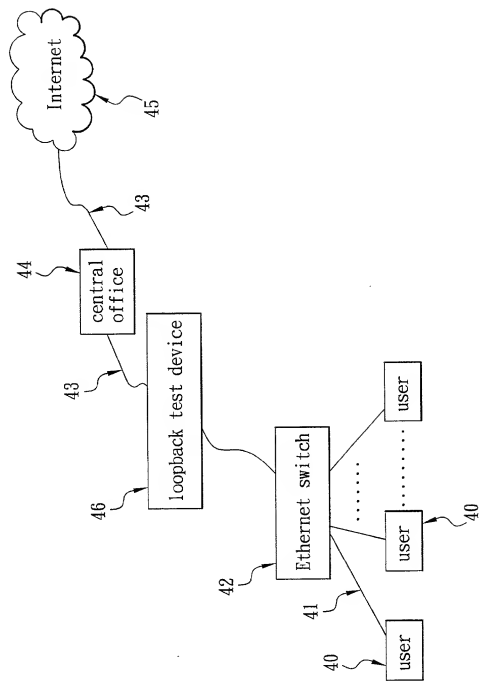


FIG. 2

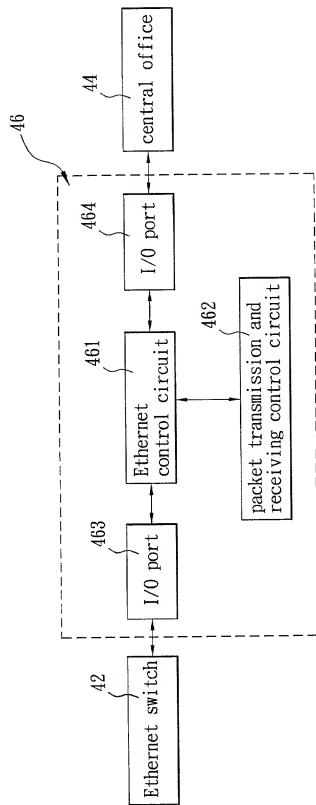


FIG. 3

MAC address (01-80-C2-00)	
MAC address (00-00)	source address (**-**-**)
source address (**-**-**-**-**)	
type (90-00)	number (00)
padding bits	
padding bits	
FCS (32 bits)	

FIG. 4

MAC address (01-80-C2-00)	
MAC address (00-00)	source address (**-**-**)
source address (**-**-**-**)	
type (90-00)	number (00)
Tx status (8 bits)	
padding bits	
FCS (32 bits)	

FIG. 5

MAC address (01-80-C2-00)		
MAC address (00-00)		source address (**-**-**)
source address (**-**-**-**-**)		
type (90-00)	number (01)	loopback test activation (00) loopback test stop (01)
padding bits		
FCS (32 bits)		

FIG. 6

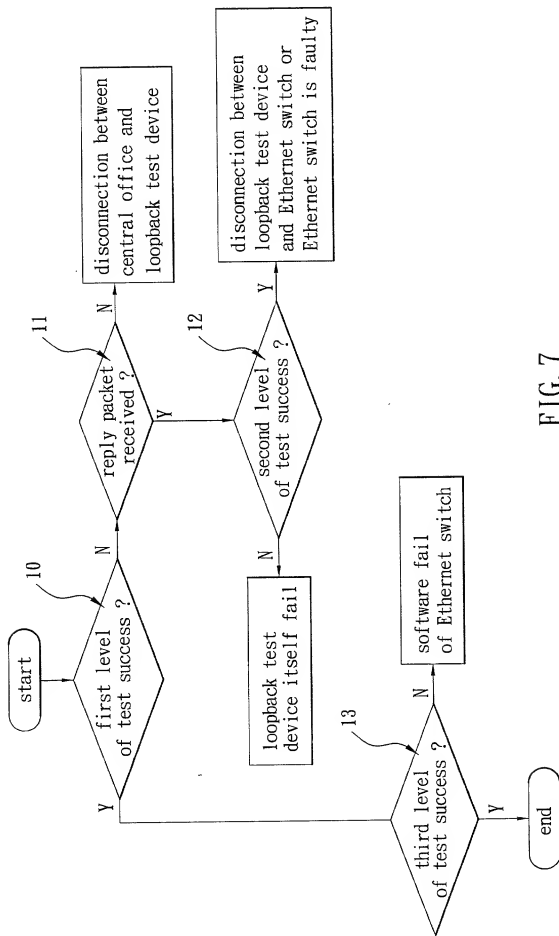


FIG. 7